

FIG. 1

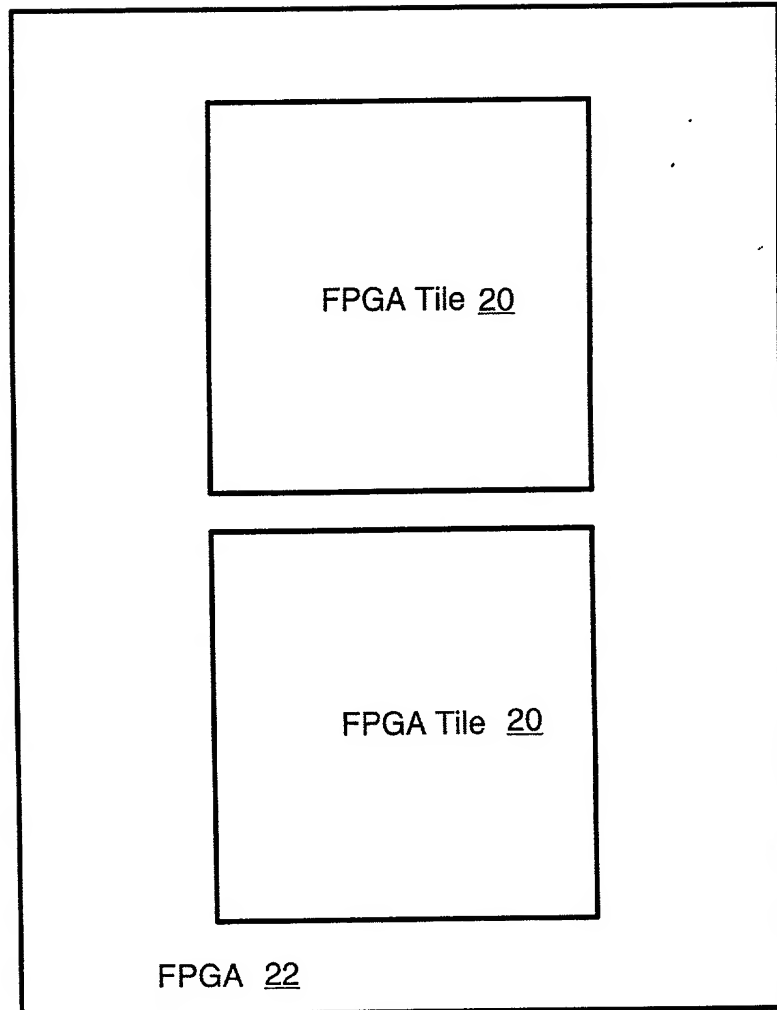


FIG. 2

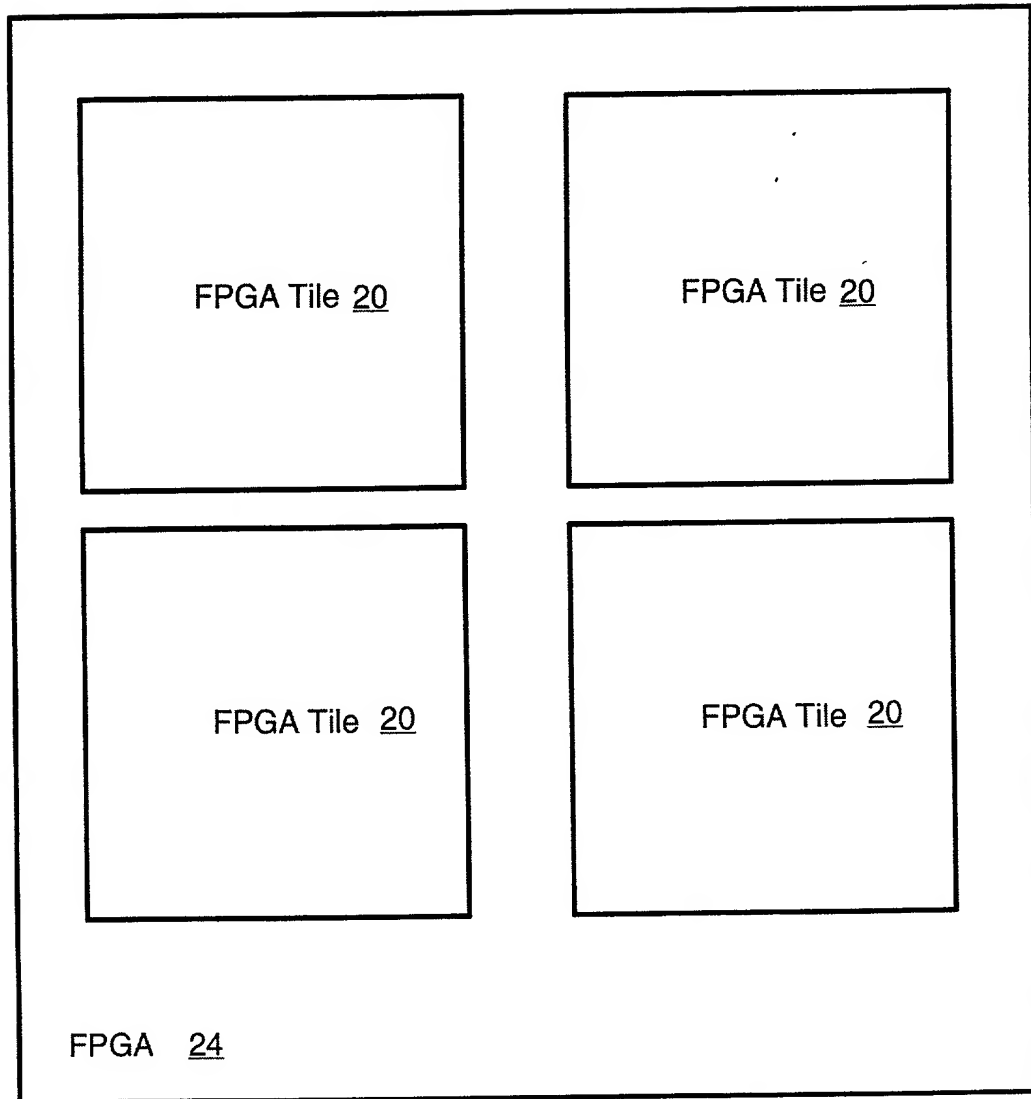


FIG. 3A

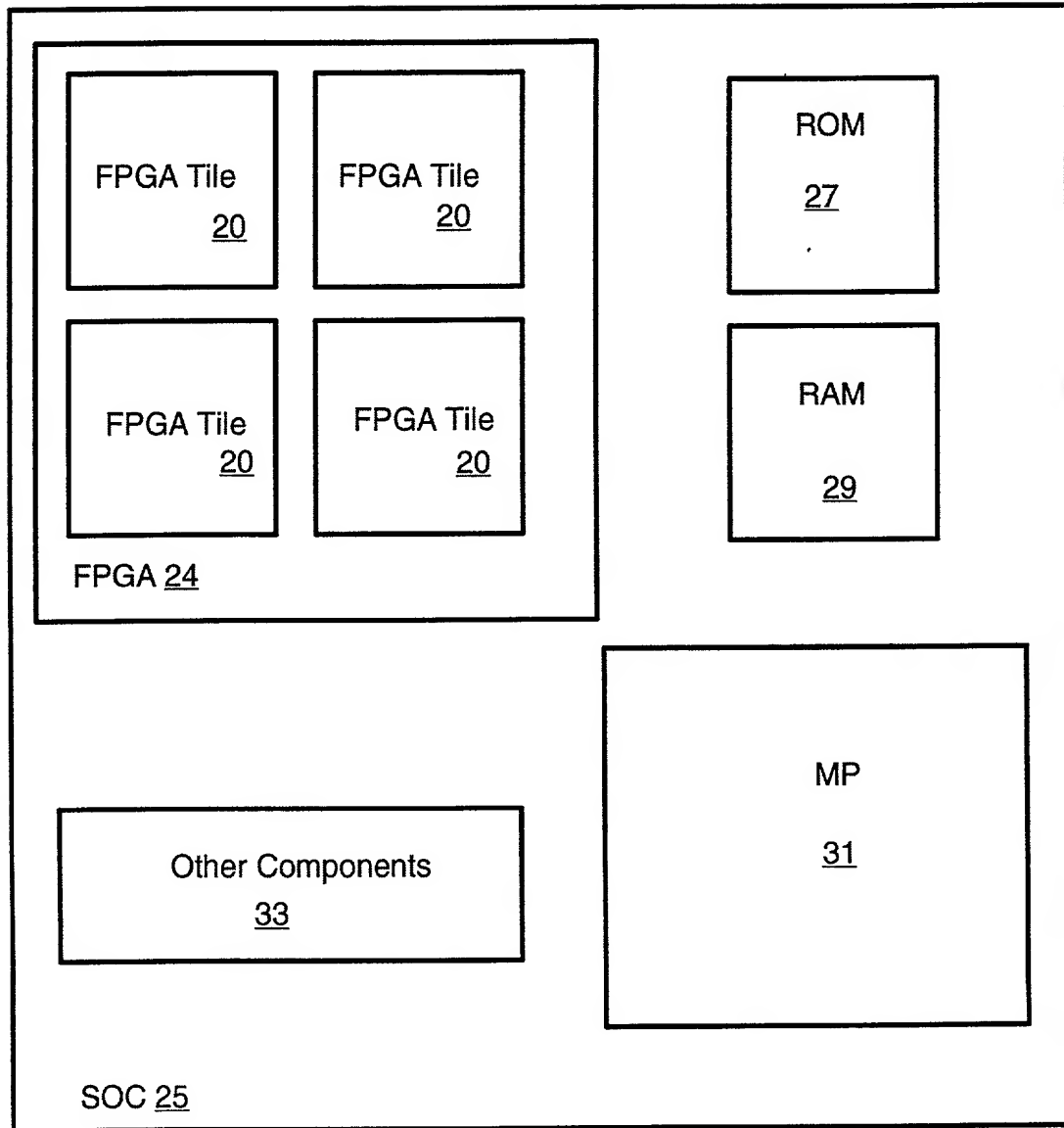


FIG. 3B

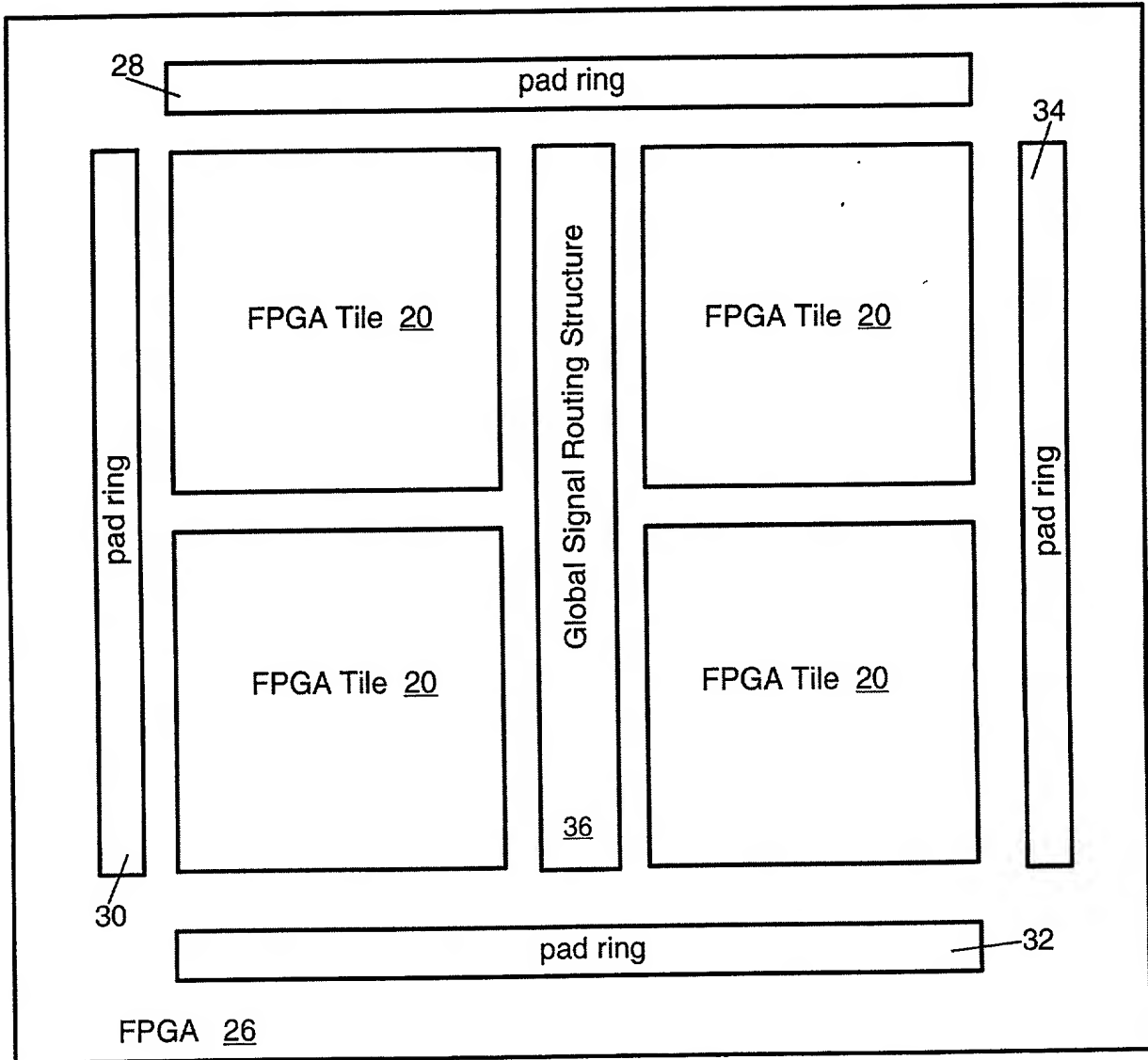


FIG. 4

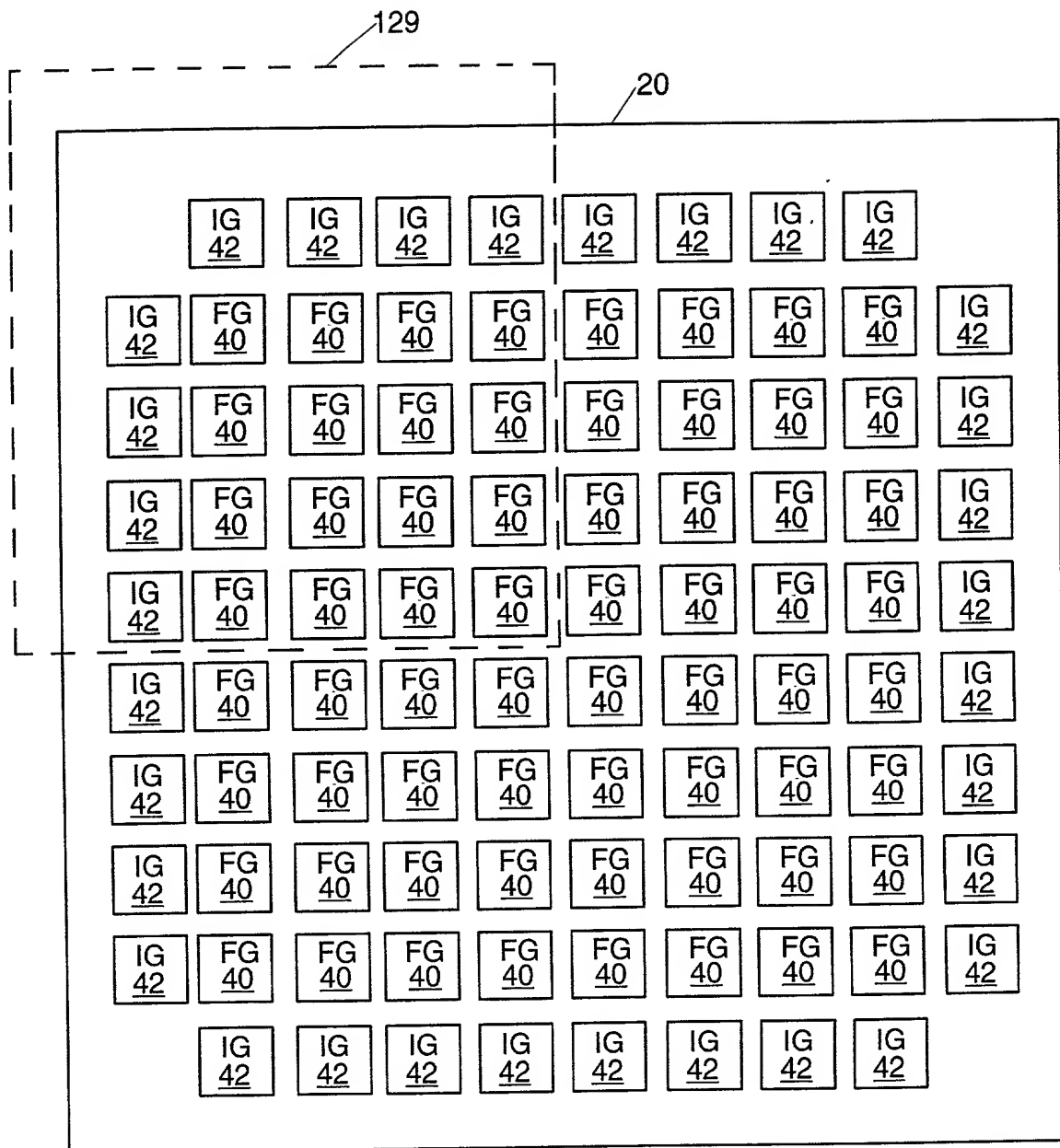


FIG. 5

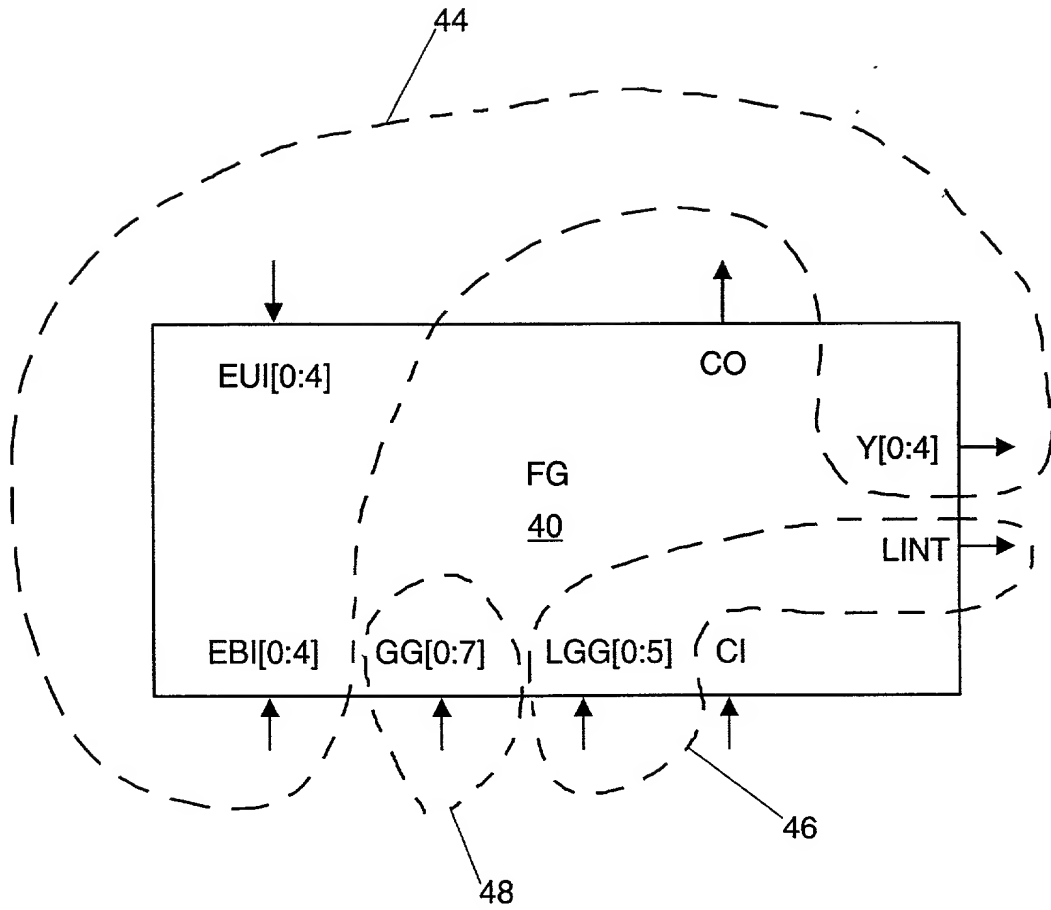
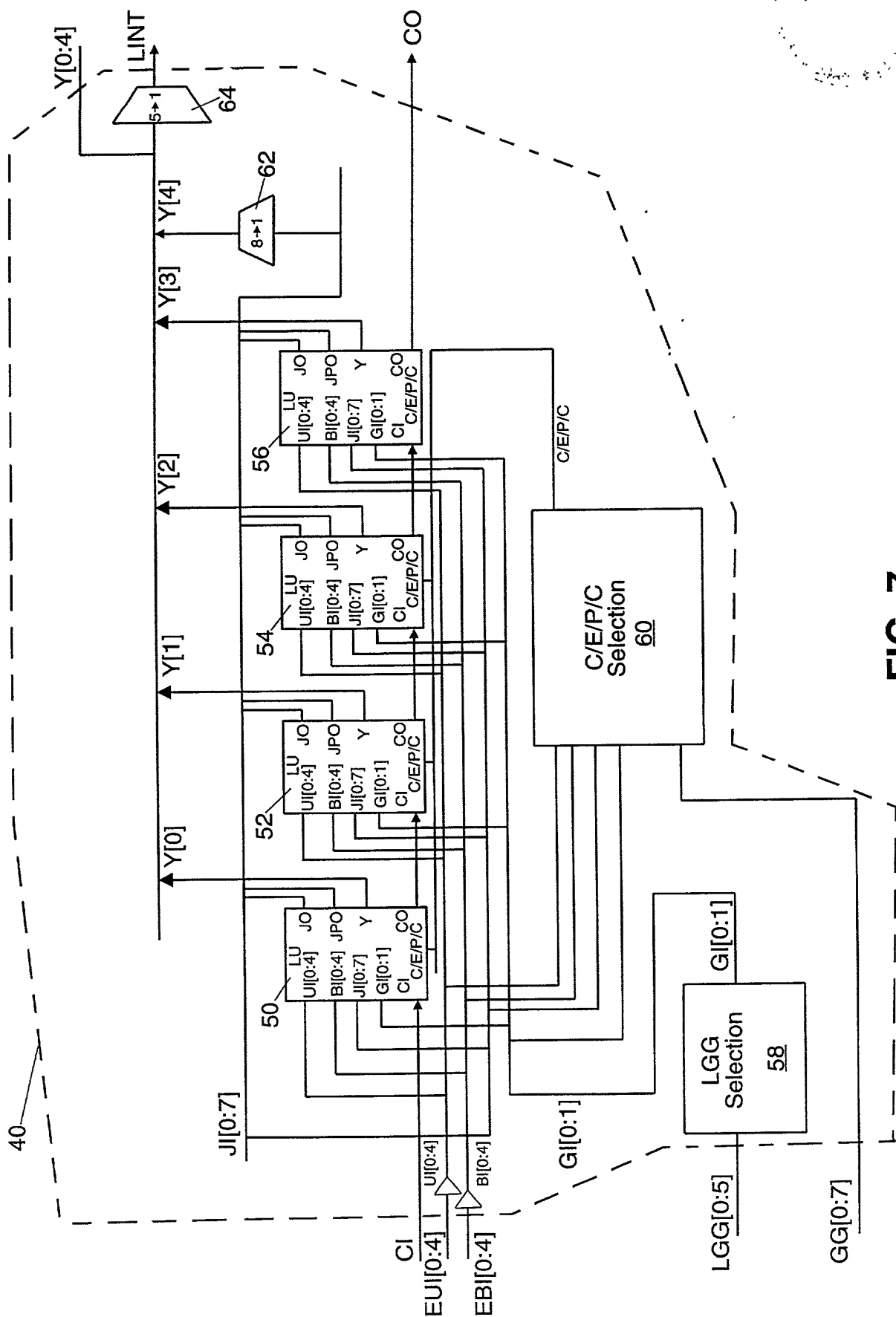


FIG. 6



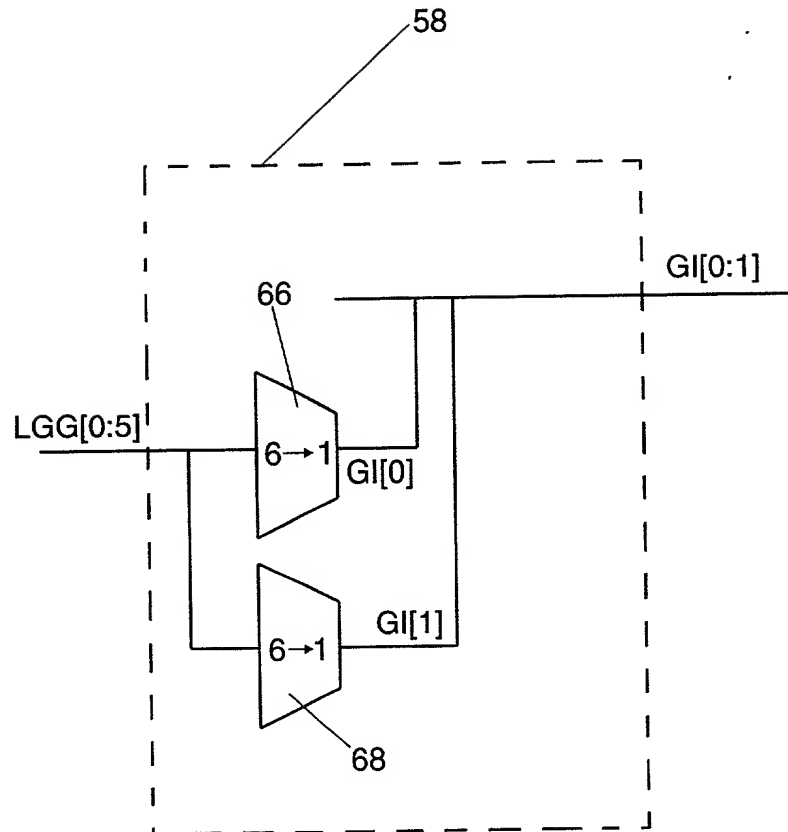


FIG. 8

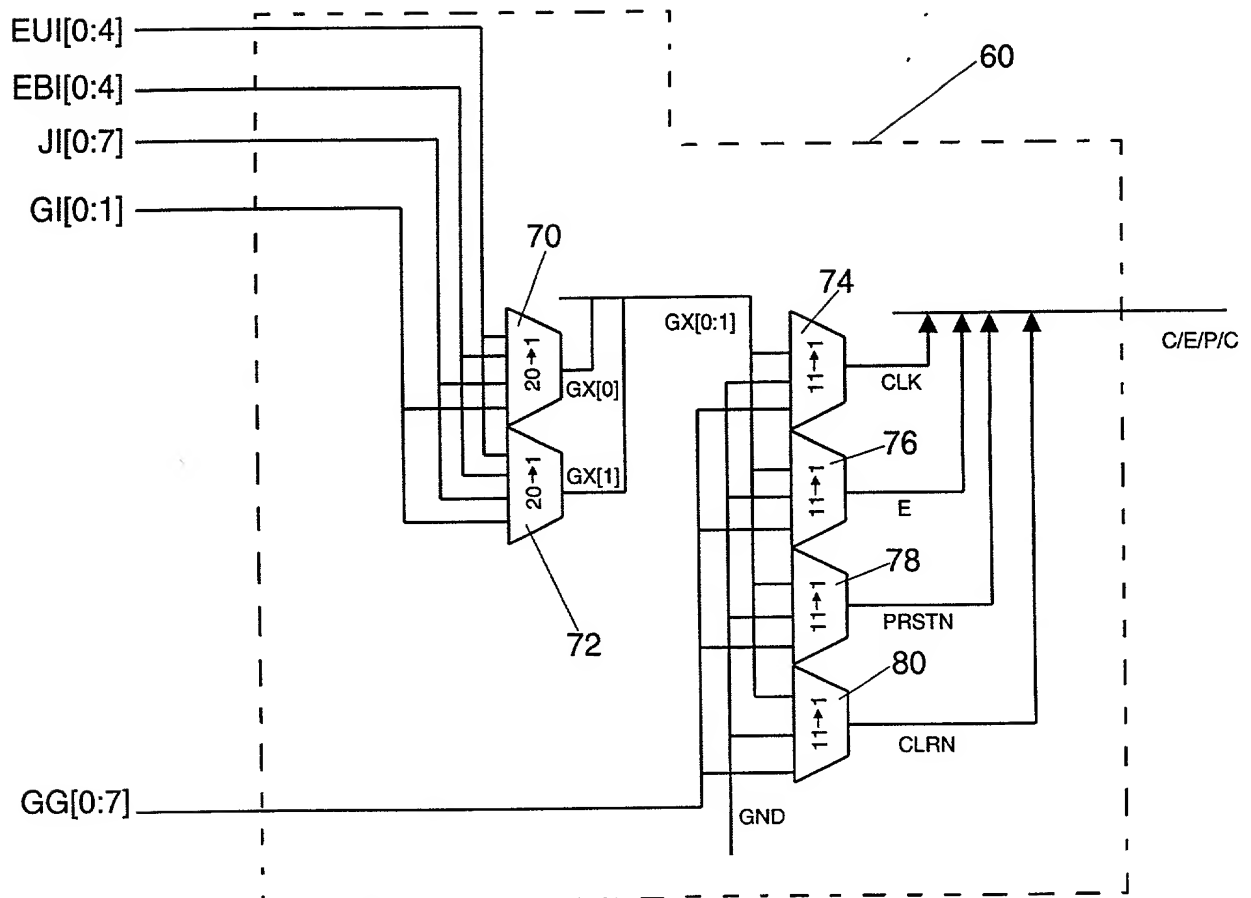


FIG. 9

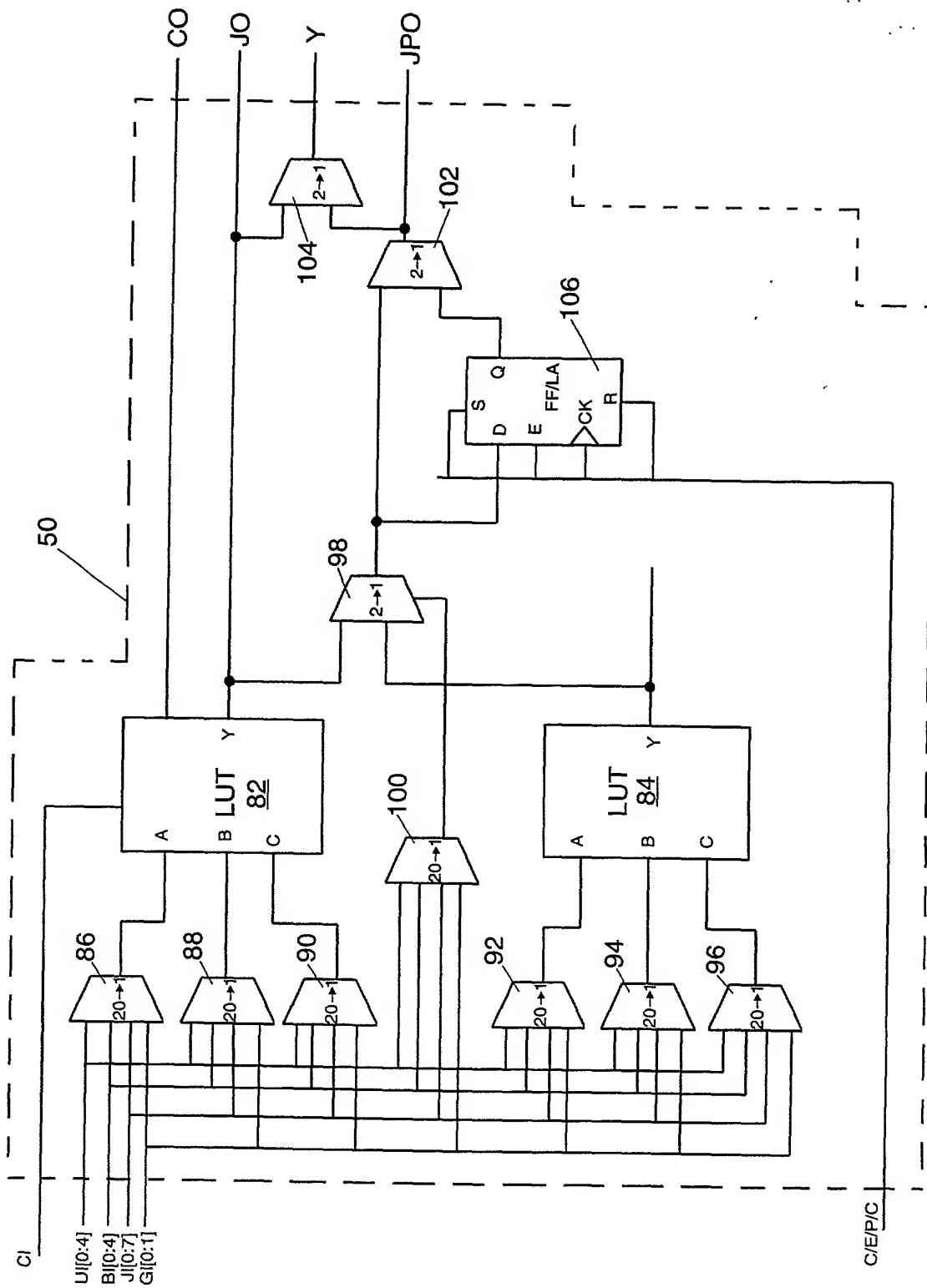


FIG. 10

FIG. 10

FIG. 11

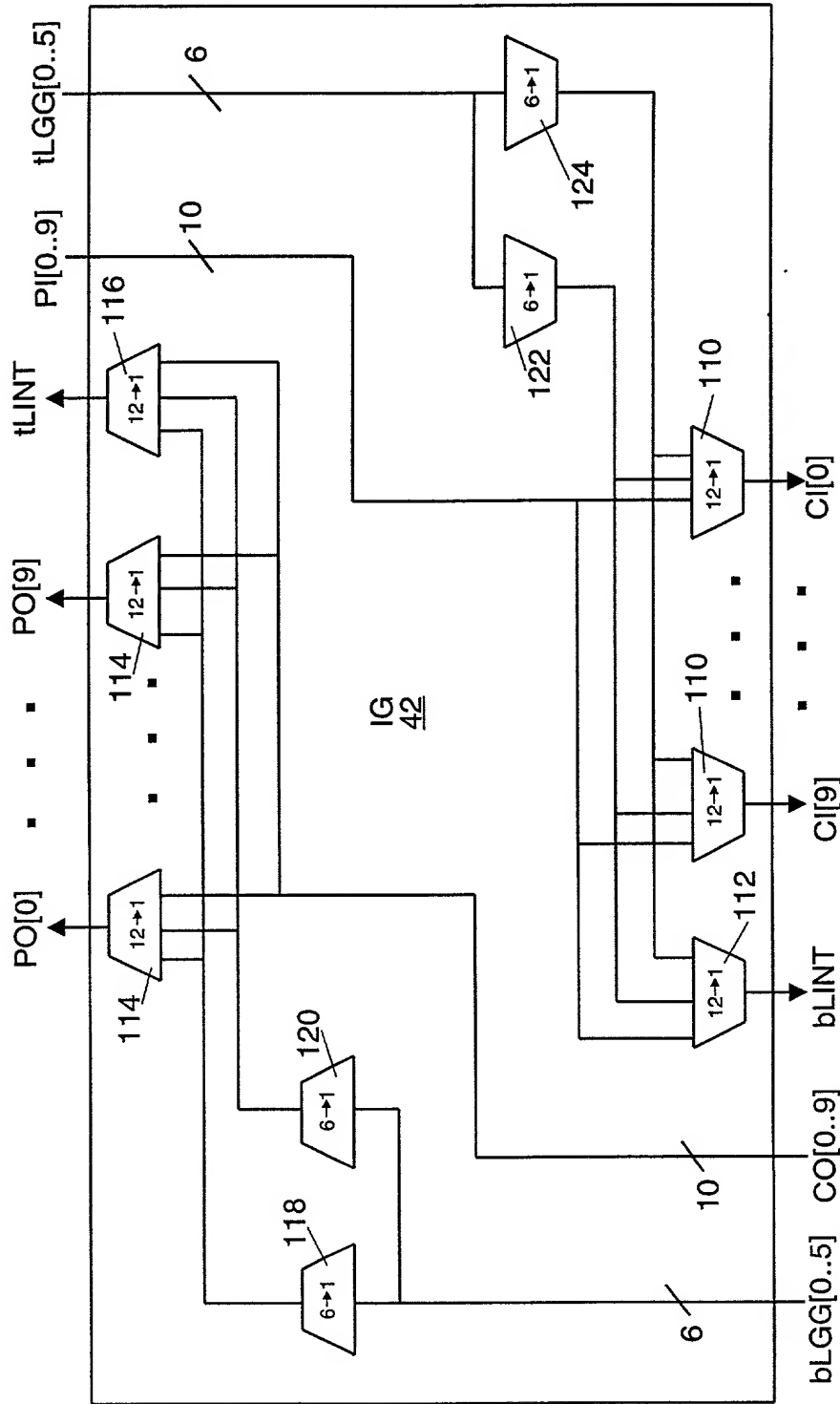


FIG. 11

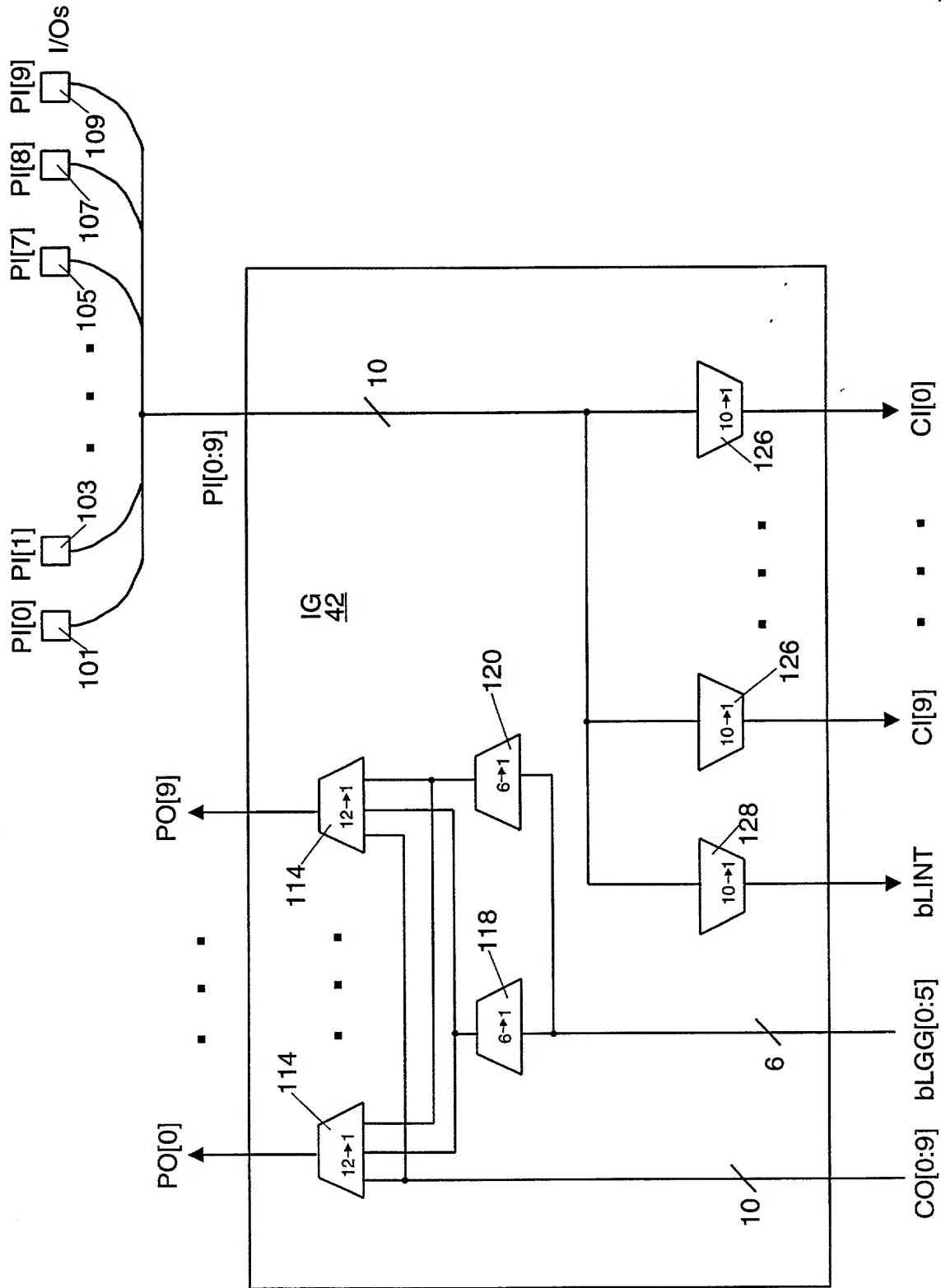


FIG. 12

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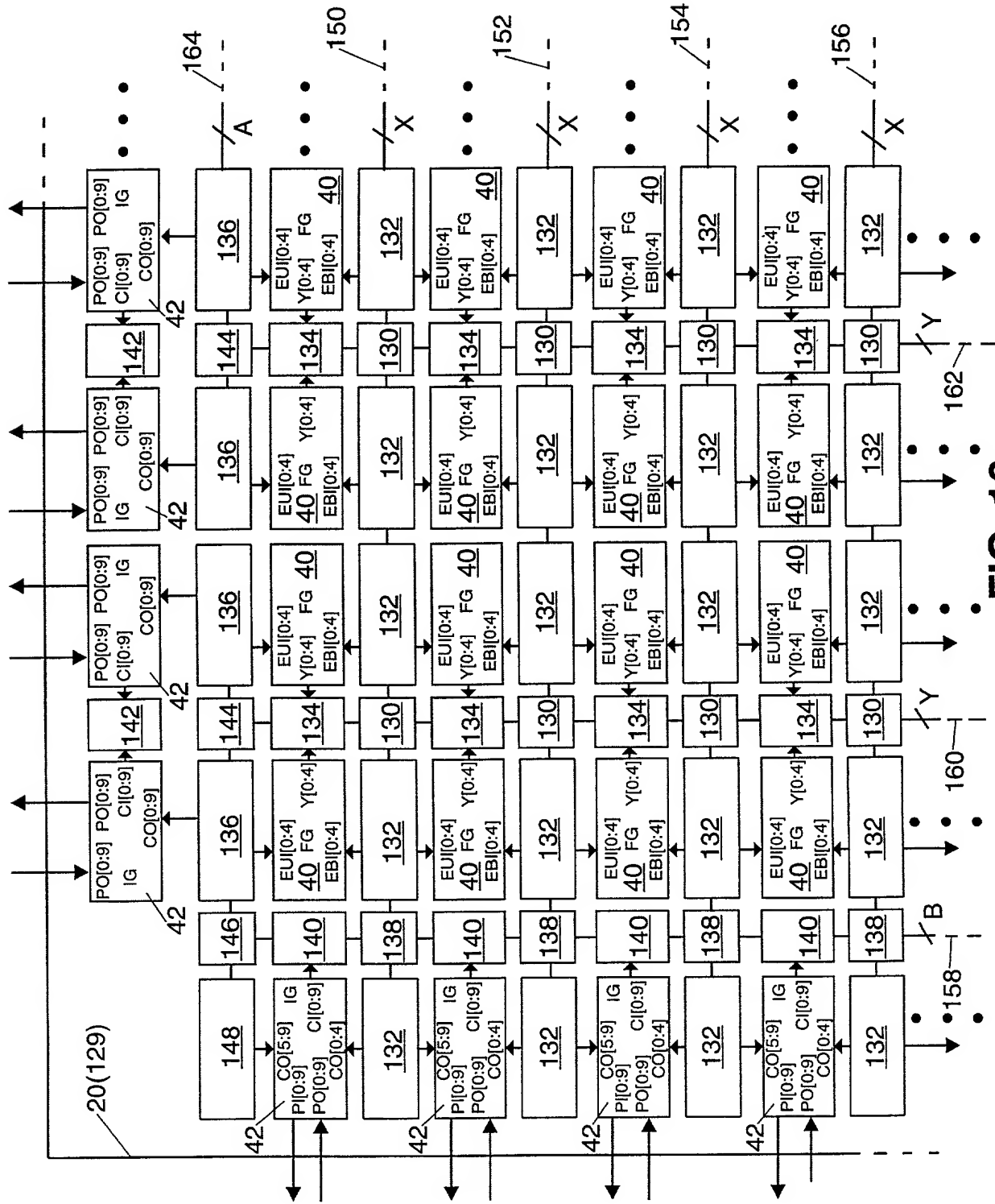


FIG. 13

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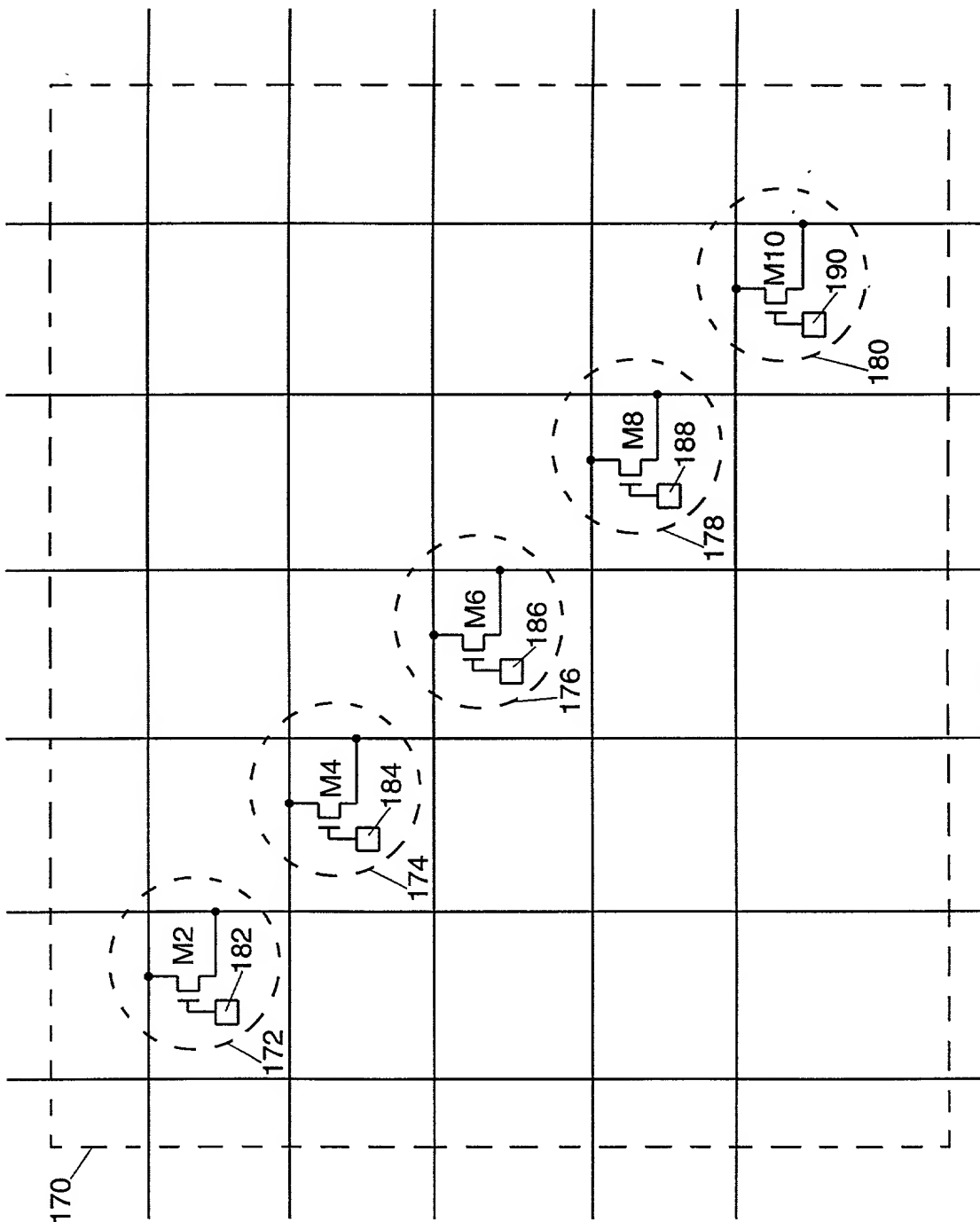


FIG. 15

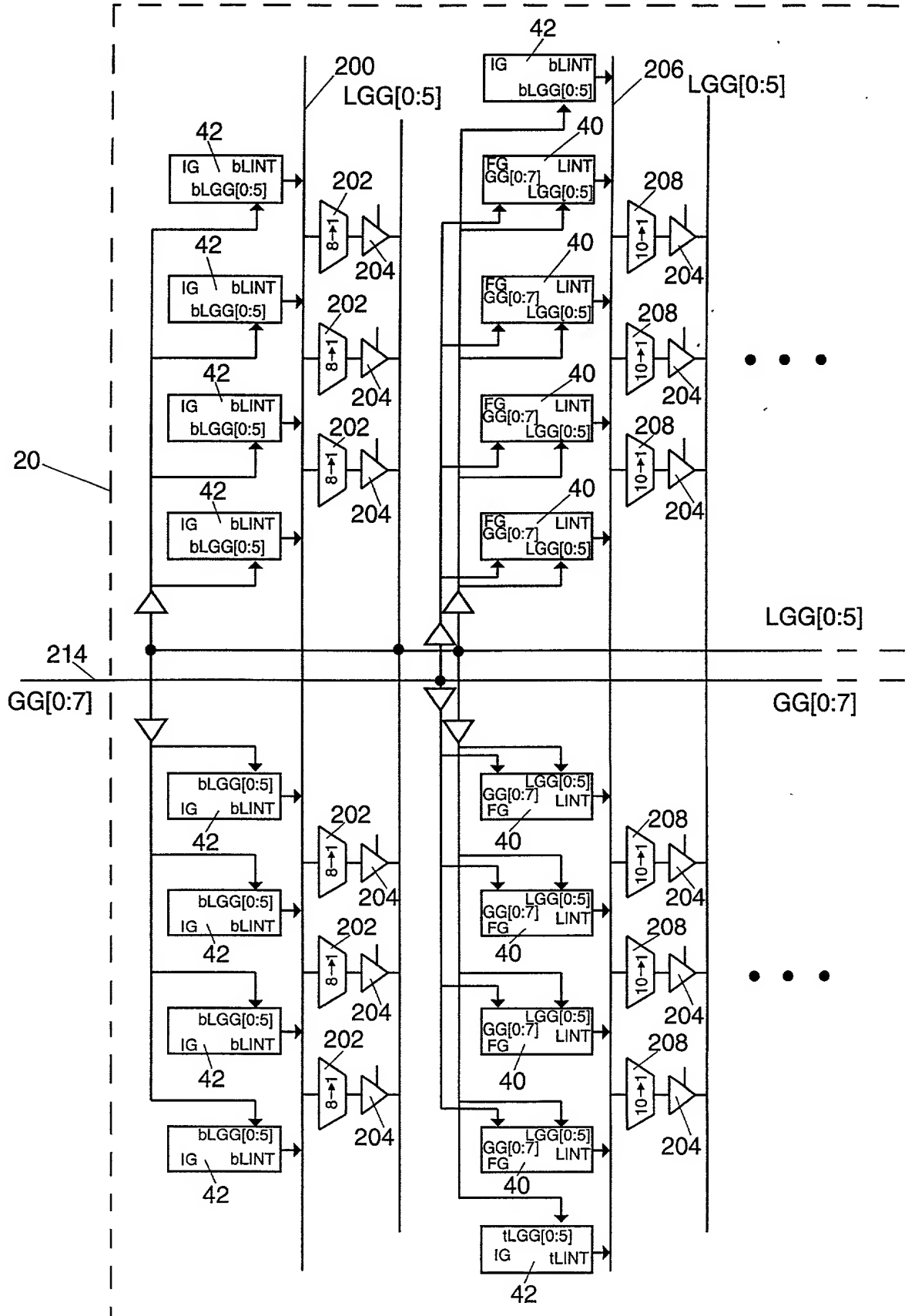


FIG. 16A

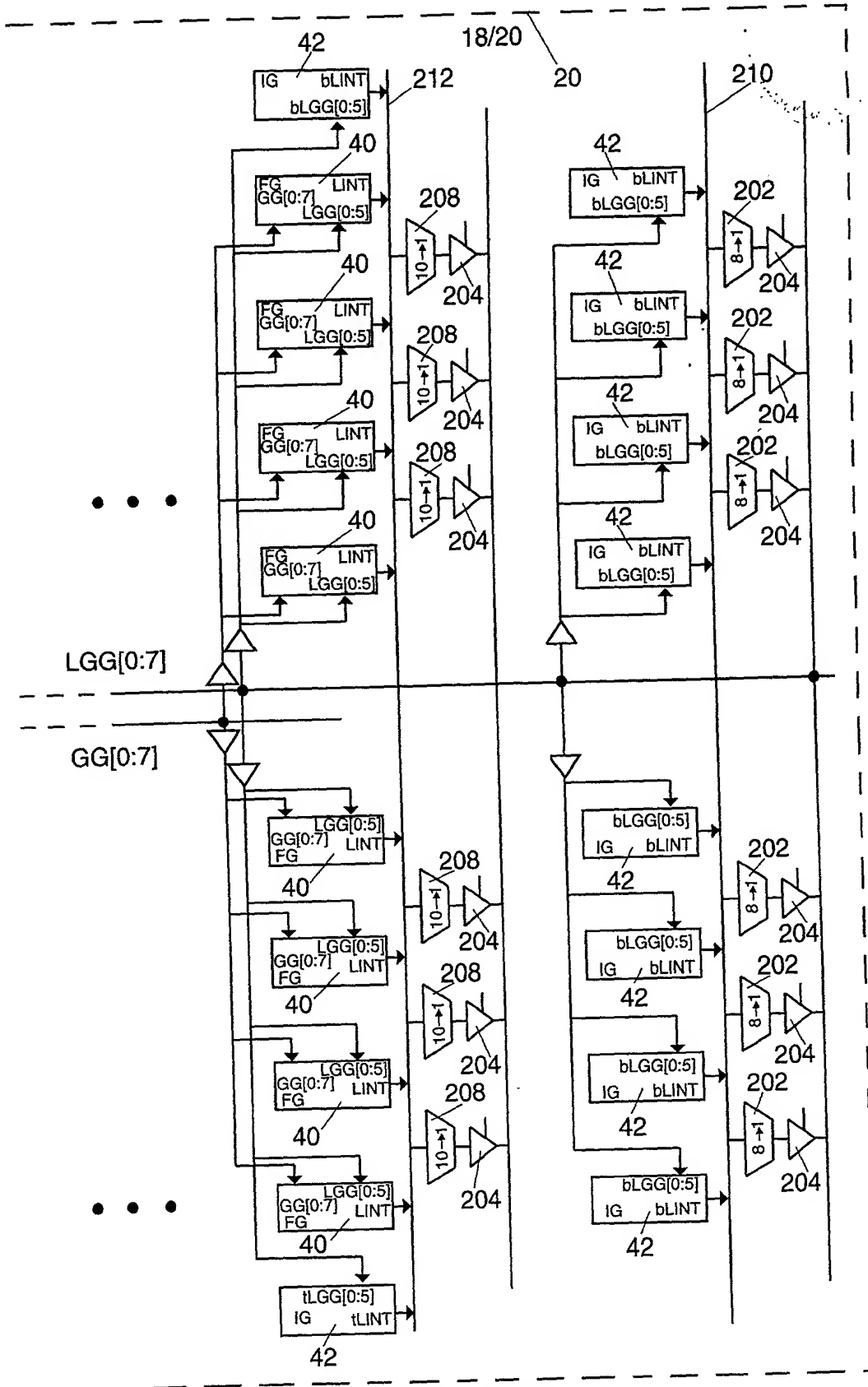


FIG. 16B

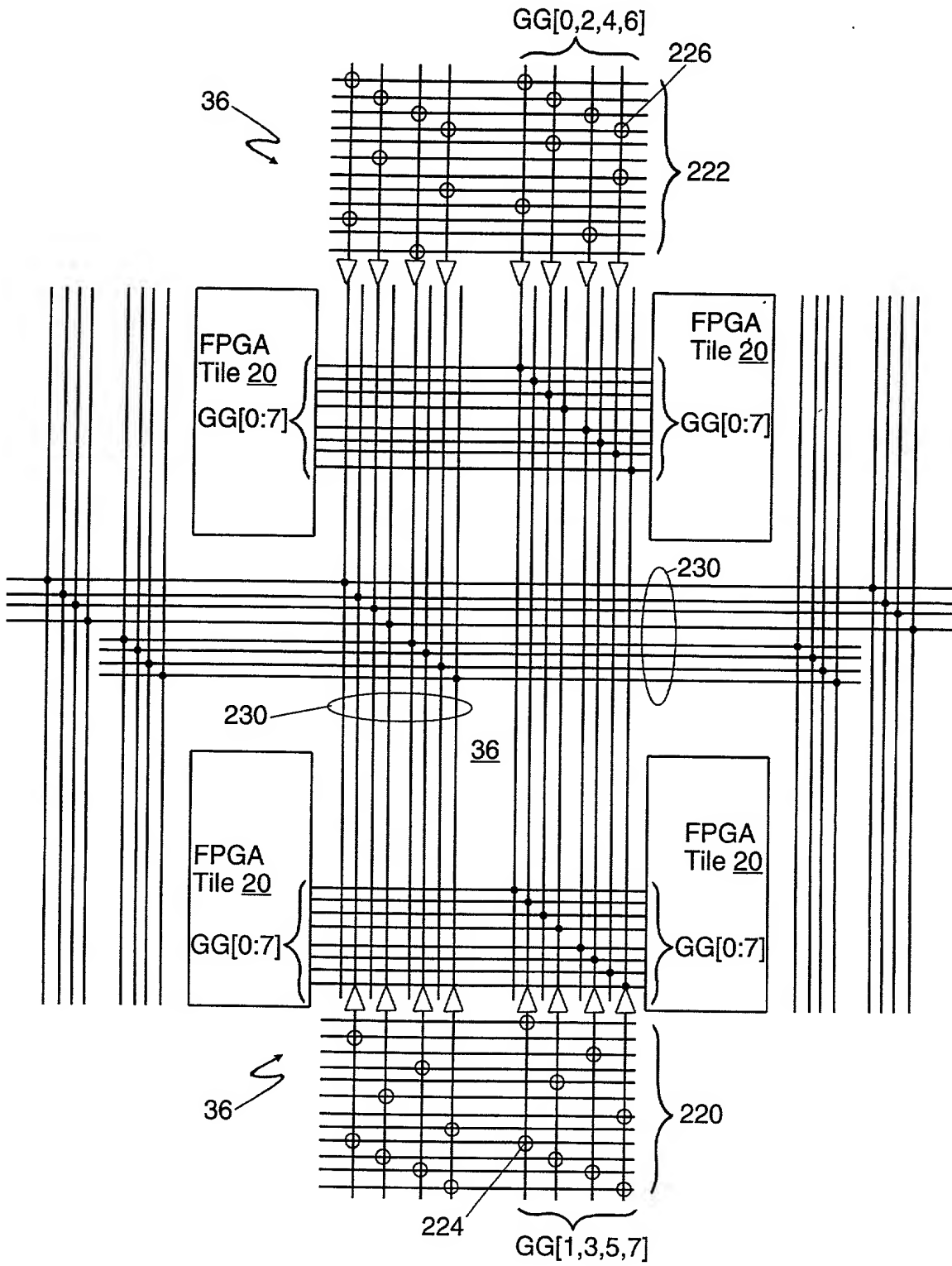


FIG. 17

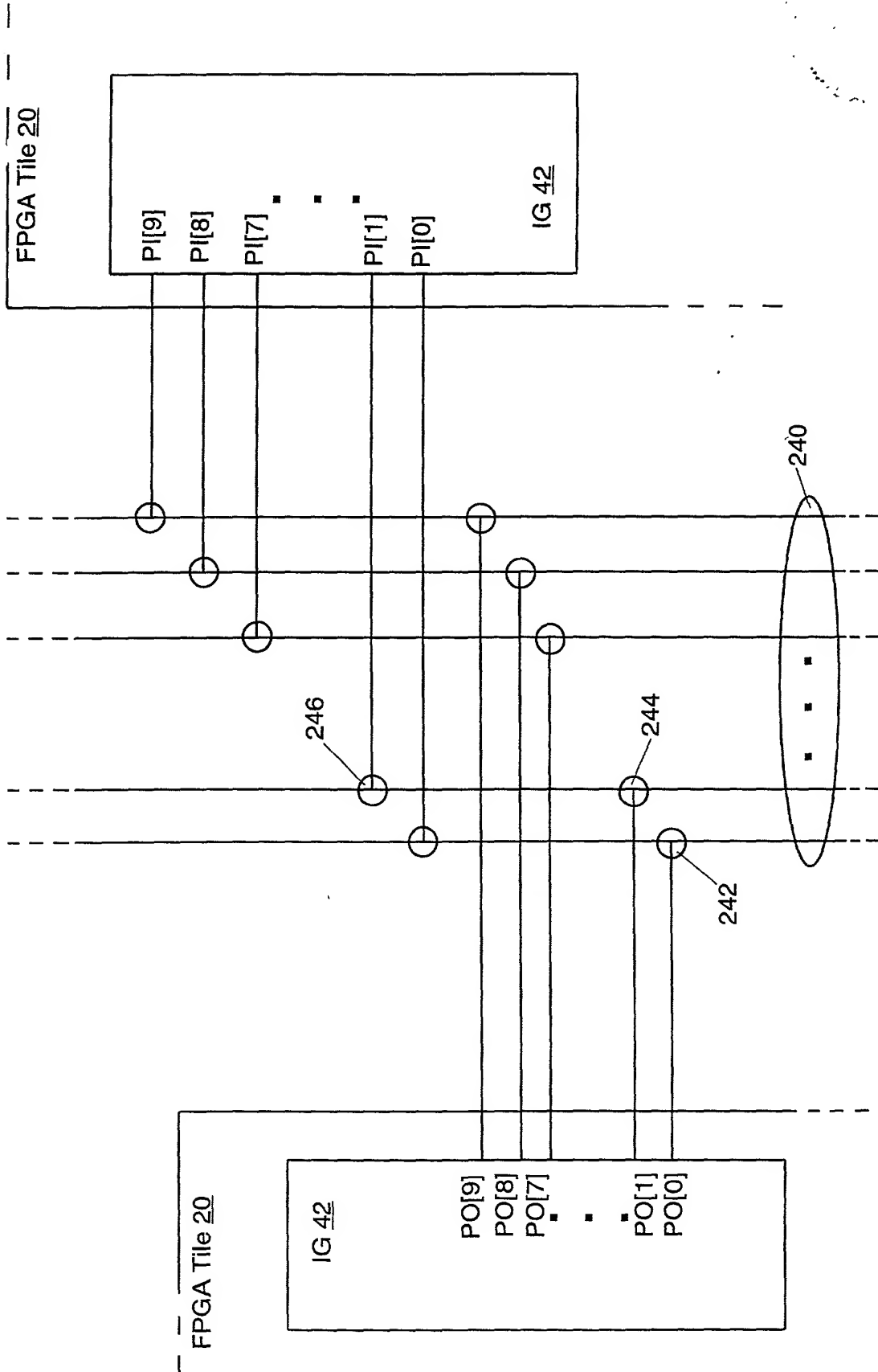


FIG. 18

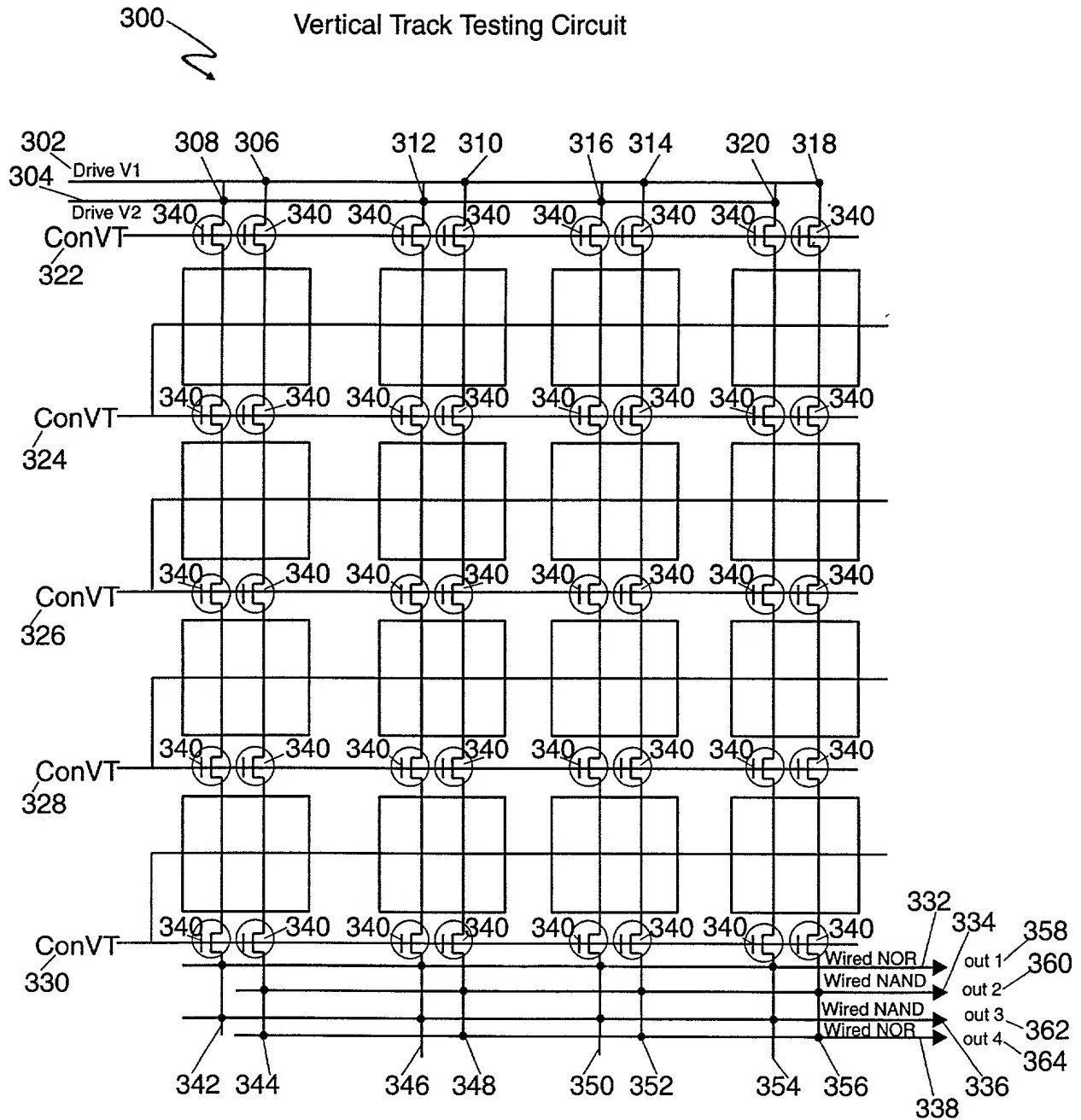


FIG. 19

| Truth Table | | | | | |
|-------------|---------|------|------|------|------|
| DriveV1 | DriveV2 | Out1 | Out2 | Out3 | Out4 |
| 0 | 0 | 1 | 1 | 1 | 1 |
| 0 | 1 | 1 | 0 | 1 | 0 |
| 1 | 0 | 0 | 1 | 0 | 1 |
| 1 | 1 | 0 | 0 | 0 | 0 |

FIG. 20

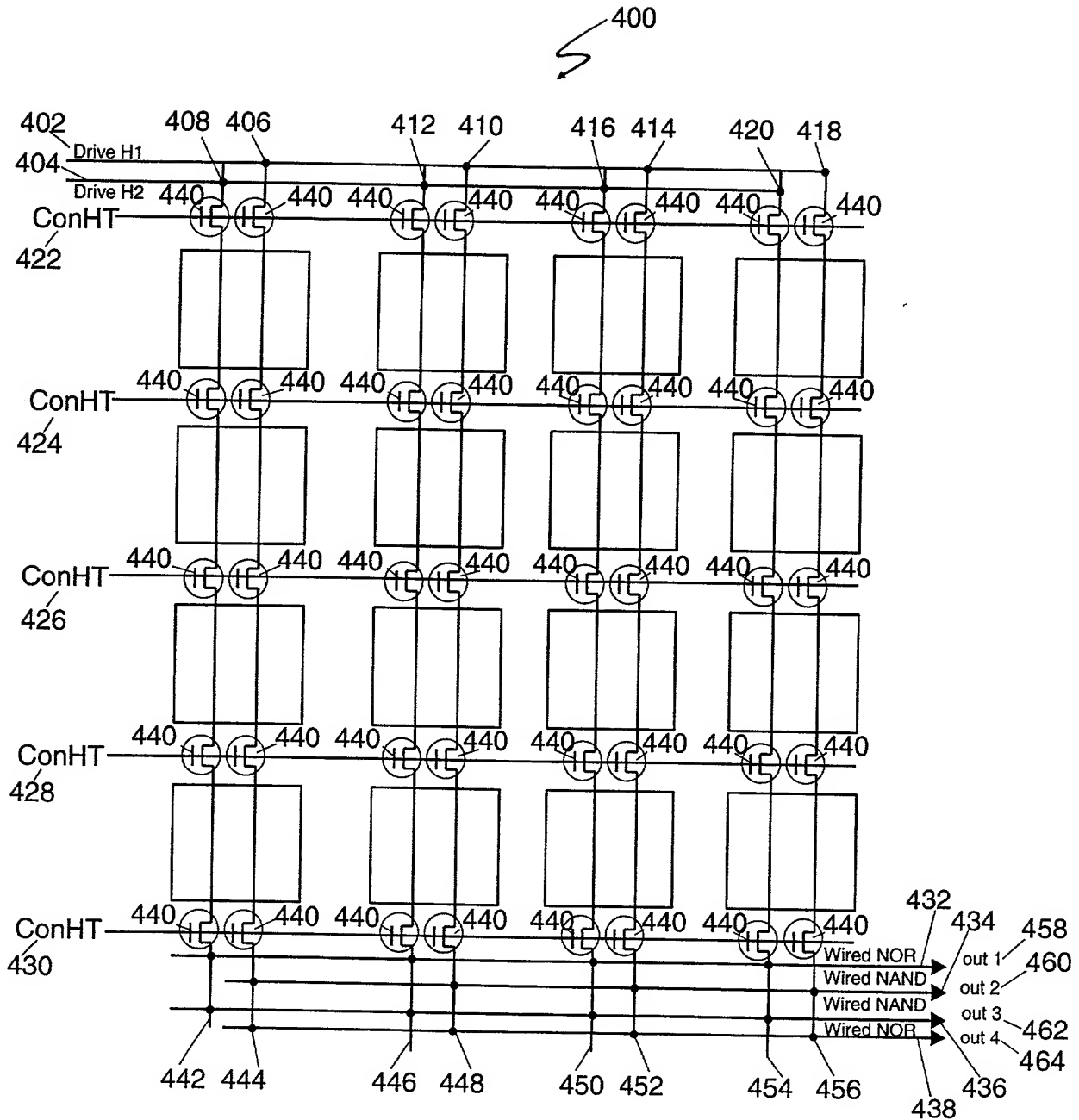


FIG. 21

| Truth Table | | | | | |
|-------------|---------|------|------|------|------|
| DriveH1 | DriveH2 | Out1 | Out2 | Out3 | Out4 |
| 0 | 0 | 1 | 1 | 1 | 1 |
| 0 | 1 | 1 | 0 | 1 | 0 |
| 1 | 0 | 0 | 1 | 0 | 1 |
| 1 | 1 | 0 | 0 | 0 | 0 |

FIG. 22